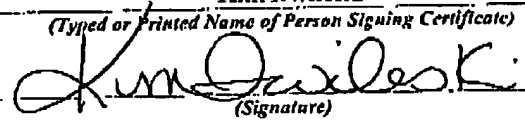


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Applicant(s): Logan et al.		
Application No. 10/687,295	Filing Date 1/7/2005	Examiner Jennifer M. Dolan
		Group Art Unit 2813
Invention: METHOD AND STRUCTURE TO REDUCE CMOS INTER-WELL LEAKAGE		
<p>I hereby certify that this _____ <u>Request for Reconsideration (13 pages)</u> _____ <i>(Identify type of correspondence)</i></p> <p>is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>703-872-9306</u> _____)</p> <p>on <u>3/10/2005</u> _____ <i>(Date)</i></p> <div style="text-align: right; margin-top: 100px;"> <u>Kim Dwileski</u> <i>(Typed or Printed Name of Person Signing Certificate)</i>  <i>(Signature)</i> </div>		
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 10/687,295

Applicant(s): Logan et al.

Filed.: January 7, 2005

Art Unit: 2813

Dkt. No.: BUR920000195US2

Examiner: Jennifer M. Dolan

Title: **METHOD AND STRUCTURE TO REDUCE CMOS INTER-WELL LEAKAGE**

Honorable Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR RECONSIDERATION

This Request for Reconsideration is being filed in response to the Office Action mailed
on January 7, 2005.

S/N 10/687,295

1